



4.1.1 Execute Stage with LMC

This topic requires knowledge of the Little Man Computer instruction set (Topic 1.2.4)

- 1 A program contains the following LMC instruction.

LDA num1

The label num1 refers to memory location **101**.

Explain how the processor executes the instructions which reference to how the registers and buses are used.

[5 marks]

- 2 A program contains the following LMC instruction.

SUB num1

The label num1 refers to memory location **101**.

Explain how the processor executes the instructions which reference to how the registers and buses are used.

[5 marks]

- 3 A program contains the following LMC instruction.

BRZ loop

The label loop refers to memory location **101**.

Explain how the processor executes the instructions which reference to how the registers and buses are used.

[3 marks]

